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Application No. 09/877,371

Amendment D

Amendments To The Claims

Claim 1 (Previously presented): A cathode plate of field emission display comprising:

- a cathode substrate of the field emission display having a thickness; and
- one or more in-laid linear isolation barriers formed within the thickness of a top surface of the cathode substrate;
- one or more electron emitter lines for emitting electrons to a display screen formed within each of the one or more in-laid linear isolation barriers,
- wherein the one or more in-laid linear isolation barriers provide field isolation between respective ones of the electron emitter lines.

Claim 2 (Canceled)

Claim 3 (Previously presented): The cathode plate of Claim 1 wherein portions of the top surface in between the one or more in-laid isolation barriers are adapted to contact gate wires of a gate frame positioned over the cathode substrate in order to dampen vibrations in the gate wires due to the driving frequency.

Claim 4 (Previously presented): The cathode plate of Claim 1 further comprising a trace coupled at one end to the top surface of the cathode substrate and coupled at an opposite end to a portion of a respective one of the one or more emitter lines.

Claim 5 (Original): The cathode plate of Claim 4 wherein the trace is bent so that the one end of the trace is flush with the top surface of the cathode plate and the opposite end is flush with the respective one of the one or more emitter lines.

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Claim 6 (Original): The cathode plate of Claim 1 wherein the one or more in-laid linear isolation barriers comprise one or more trenches.

Claim 7 (Original): The cathode plate of Claim 1 wherein regions of the top surface of the cathode plate in between the one or more in-laid linear isolation barriers are adapted to contact gate wires of a gate frame of the field emission display and dampen vibrations in the gate wires from the driving frequency.

Claim 8 (Original): The cathode plate of Claim 1 wherein the one or more in-laid linear isolation barriers extend a full length of the cathode substrate.

Claims 9-10 (canceled)

Claim 11 (Previously presented): The cathode plate of claim 1 wherein portions of the top surface of in between the one or more in-laid linear isolation barriers are adapted to contact a gate structure extending over the one or more in-laid linear isolation barriers.

Claim 12 (Previously presented): The cathode plate of Claim 1 wherein each electron emitter line comprises a separate and discrete continuous line extending across the cathode substrate.

Claim 13 (Canceled)

Claim 14 (Previously presented): The cathode plate of claim 1 further comprising a gate structure extending over the one or more in-laid isolation barriers.

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Claim 15 (Previously presented): The cathode plate of claim 1 wherein each electron emitter line comprises a plurality of emitter portions deposited on a surface within an in-laid isolation barrier, wherein there is no separating structure positioned in between adjacent emitter portions on the surface within the in-laid isolation barrier.

Claim 16 (Cancelled)

Claim 17 (Previously presented): The cathode plate of claim 1 further comprising a gate structure adapted to cause an electron emission from an emitter line to the display screen.

Claims 18-20 (Canceled)

Claim 21 (Previously presented): The cathode plate of claim 1 further comprising:

the display screen; and

a plurality of phosphor lines coupled thereto, the electron emitter lines emitting electrons to the plurality of phosphor lines.

Claim 22 (Canceled)

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